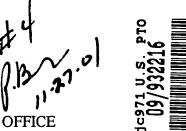
SECTION 2:

FORM PTO 1449 - MODIFIED



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Yang

Atty Dkt:

Class

2550/111

Serial No:

Not yet assigned

Date:

August 17, 2001

Date Filed:

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Document

Number

Invention:

Exam. Ref.

No.

Init.

Bootstrap Module for Multi-Stage Circuit

Issue

Date

Commissioner for Patents Washington, DC 20231

LIST OF PATENTS AND PUBLICATIONS FOR APPLICANTS' INFORMATION DISCLOSURE STATEMENT

U.S. Documents

Name

	AA AB	6,060,937 6,118,326	May 9, 2000 Sept. 12, 2000	Singer, et. al. Singer, et. al.	327/390 327/390	
	Other Documents					
Exam. <u>Init.</u>	Ref.	<u>Name</u>				
	AC	•	1.5-V, 10-bit, 14.3-MS/s CMOS Pipeline Analog-to-Digital Converter", Abo, et. al., EE Journal of Solid-State Circuit, Vol. 34, No. 5, pp. 599-605, May 1999.			
Examiner:						

NOTE FOR EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; draw line through citation if not in conformance AND not considered. Include copy of this form with next communication to applicant.

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Date Considered: